

FIG. 1

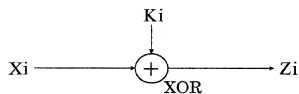


FIG. 2

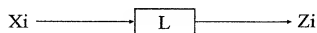


FIG. 3

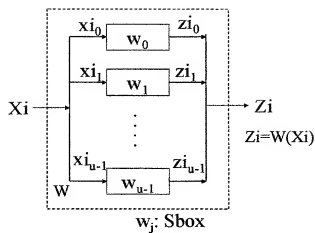


FIG. 4

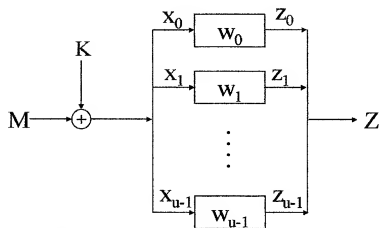


FIG. 5

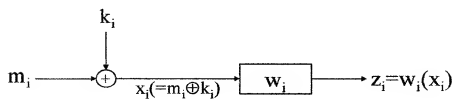
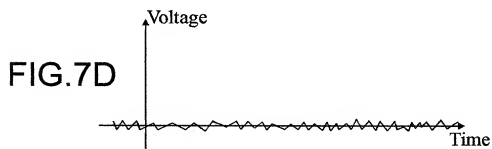
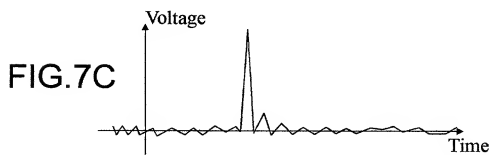
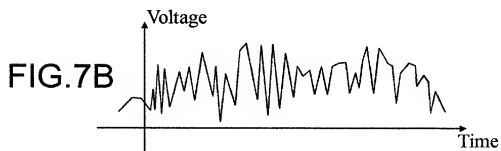
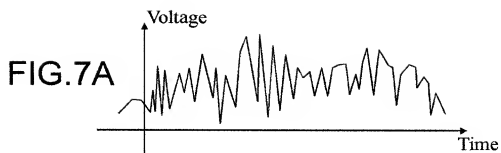


FIG. 6



The diagram shows a parallel neural network structure. An input M is added to a feedback signal B at a summation node (circle with a plus sign) to produce a common input C . This input C is then distributed to u parallel processing units. Each unit i takes C as input x_i and produces an output z_i through a block w_i . The outputs z_0, z_1, \dots, z_{u-1} are then summed to produce the final output Z . The feedback signal B is derived from the output Z .

FIG. 9

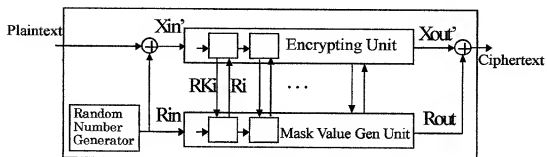
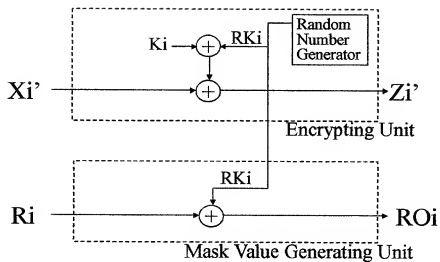


FIG. 10



Key XOR in Random Mask Value Method

FIG. 11

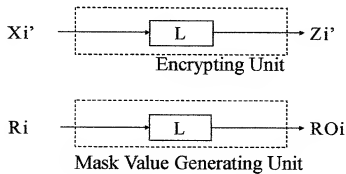


FIG. 12

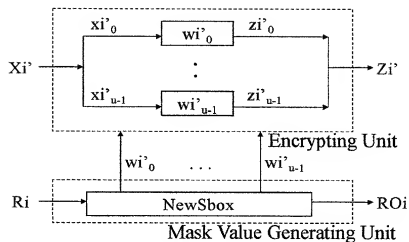


FIG. 13A

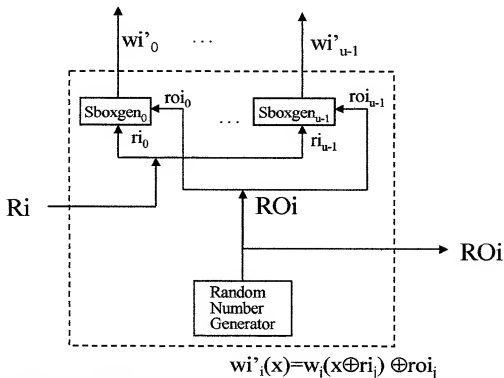


FIG. 13B

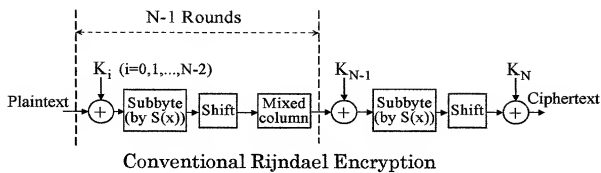
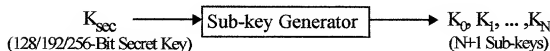


FIG. 14



Generation of Sub-keys in Rijndael Encryption

FIG. 15

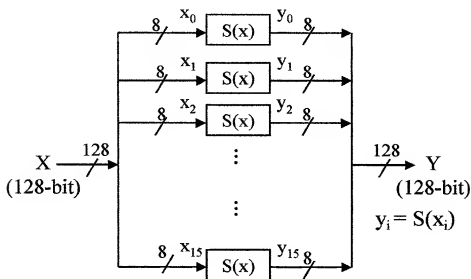


FIG. 16

Subbyte

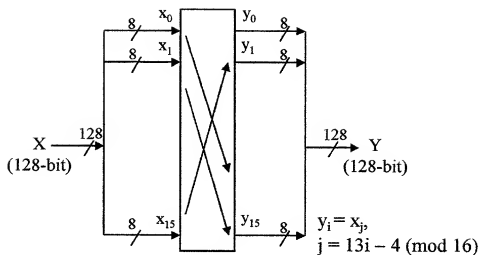


FIG. 17

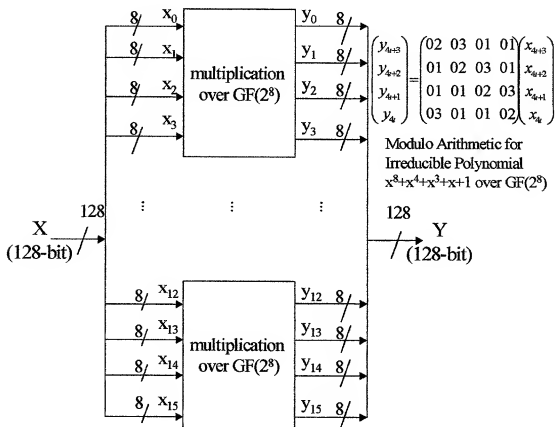


FIG. 18

Mixedcolumn

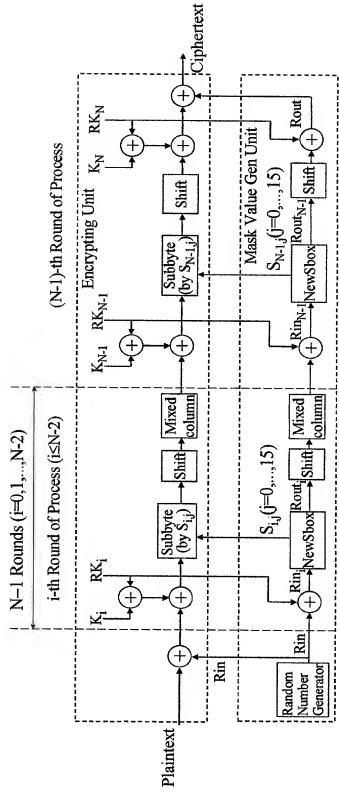
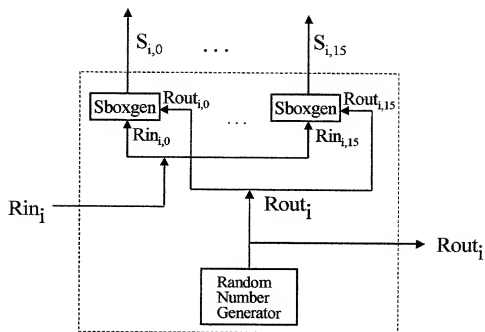


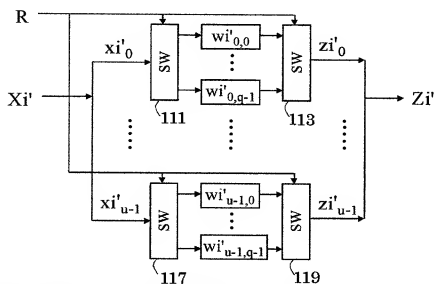
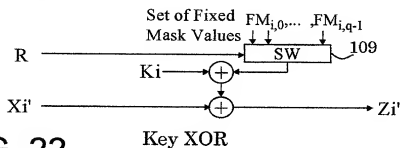
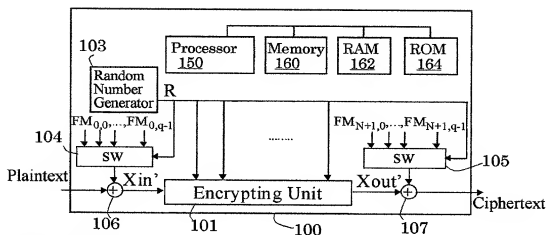
FIG. 19



Sboxgen Generates Sbox, $S_{i,j}$, such that $S_{i,j}(x) = S(x \oplus Rin_{i,j}) \oplus Rout_{i,j}$

NewSbox

FIG. 20



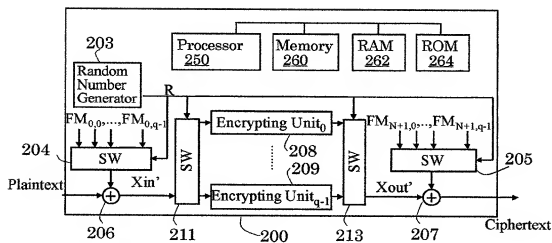


FIG. 24

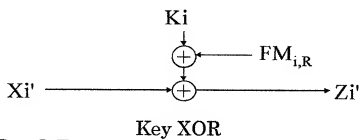


FIG. 25

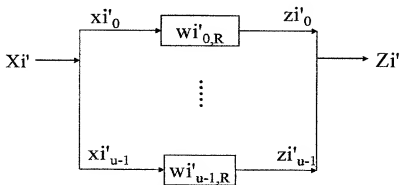


FIG. 26 Nonlinear Transform

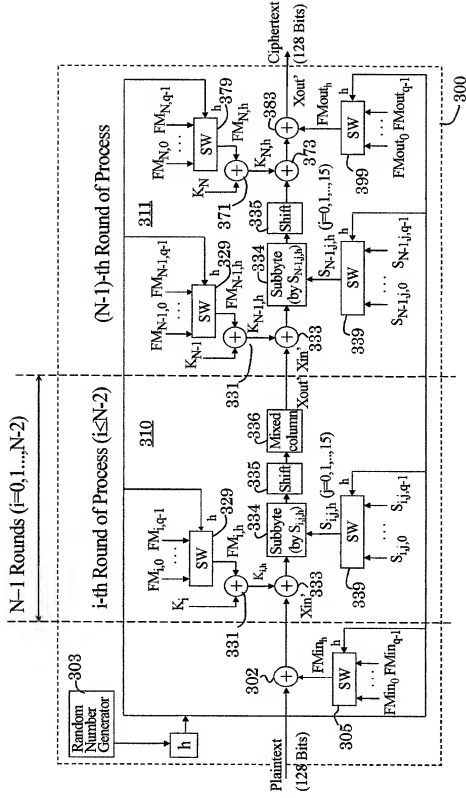
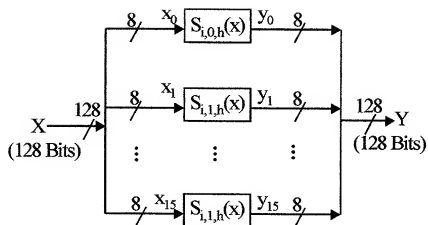


FIG. 27



$$S_{i,j,h}(x) = S(x \oplus c_{i,j,h}) \oplus d_{i,j,h}$$

$S(x)$: Sbox in Conventional Rijndael Process
Subbyte

FIG. 28

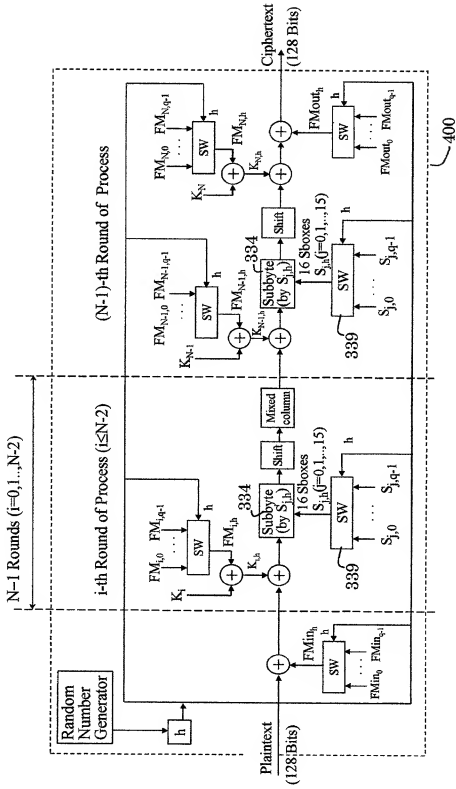
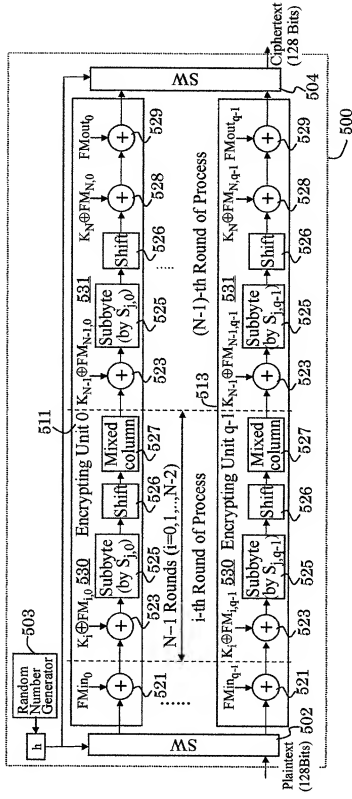
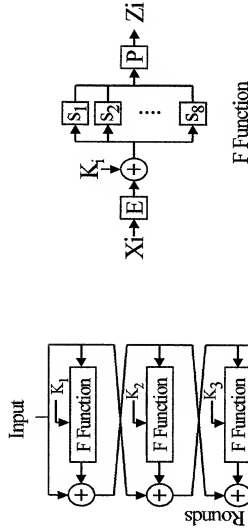


FIG. 29



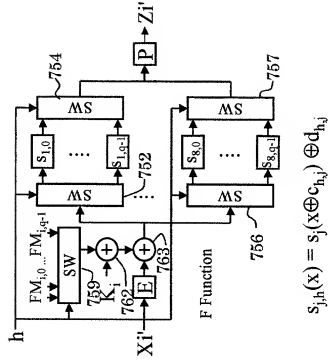
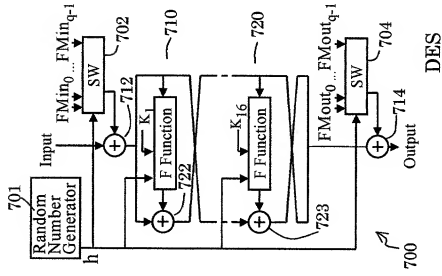


E : Linear Transform
 $S_1 \dots S_8$: Table of Nonlinear Transform

DES

FIG. 32B

FIG. 32A



$$s_{j,h}(x) = s_j(x \oplus c_{h,j}) \oplus d_{h,j}$$

FIG. 33A

FIG. 33B

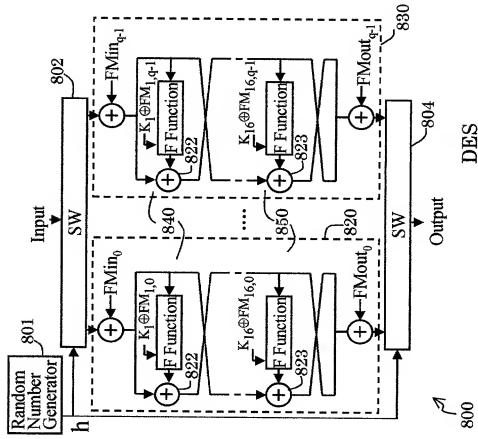


FIG. 34A

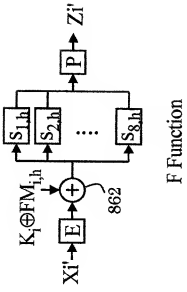
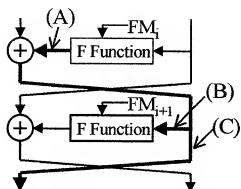
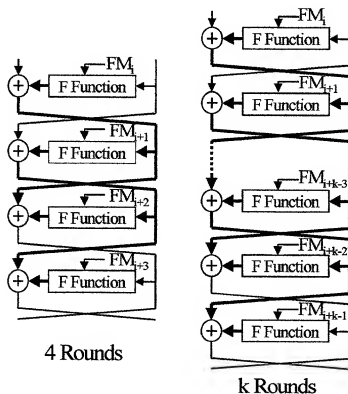


FIG. 34B



Propagation of Mask in Feistel Encryption

FIG. 35



Paths from Mask Value Generation to Cancellation in Feistel Encryption Device

FIG. 36